LIBRARY OF CELLS FOR USE IN DESIGNING SETS OF DOMINO LOGIC CIRCUITS IN A STANDARD CELL LIBRARY, OR THE LIKE, AND METHOD FOR USING SAME

Abstract

A cell library for designing integrated domino circuits has a first library portion with a plurality of selectable logic circuits having different transistor sizes and/or logic functions for selection according to desired logic function and parametric characteristics. A second library portion includes a plurality of selectable prechargeable driver circuits. Each of the driver circuits is configured to be connectable to an output of a selected one of the logic circuits. The driver circuits also have at least different transistor sizes. Standard FET devices may be constructed to precharge the output node of the selected logic circuit in the design of a domino logic circuit.